Image

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Takahiko SATO

Atty. Docket No.: 108397-00042

Serial No.: 09/843,820

Group Art Unit: 2816

Filed: April 30, 2001

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CIRCUIT FOR CHANGING TIMING OF INACTIVATING POWER-ON RESETTING CIRCUIT

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

April 2, 2004

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated <u>October 3, 2003</u> of the Primary Examiner finally rejecting Claims <u>1 and 2</u>.

The items checked below are appropriate:

- 1. XX A petition for an extension of time to respond to the final rejection is enclosed for three months.
- 2. XX A timely response to the final rejection was filed January 2, 2004, as provided in 841 O.G. 1411
- 3. XX A check in the amount of \$1,280.00 to cover the Notice of Appeal Fee of \$330.00 and the Extension Fee of \$950.00 is enclosed.

In the event that this paper is not considered to be timely filed, applicant hereby petitions for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2300, referencing Attorney Docket No. 108397-00042, along with any other additional fees which may be required with respect to this paper.

04/06/2004 TBESHAH1 00000024 09843820

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330.00 OP

Timothy J. Churna

ARENT FOX PL

Registration No.: 48,340

Respectfully submitted

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TJC/mso

Enclosure: Check # 401957